

THYRISTOR HAVING ONE OR MORE  
DOPED LAYERS

By: Tatsing Paul Chow and Jeffrey Bernard Fedison  
Attorney Docket No.: R0434/7008 EJR  
August 31, 2001  
EL 711222020 US

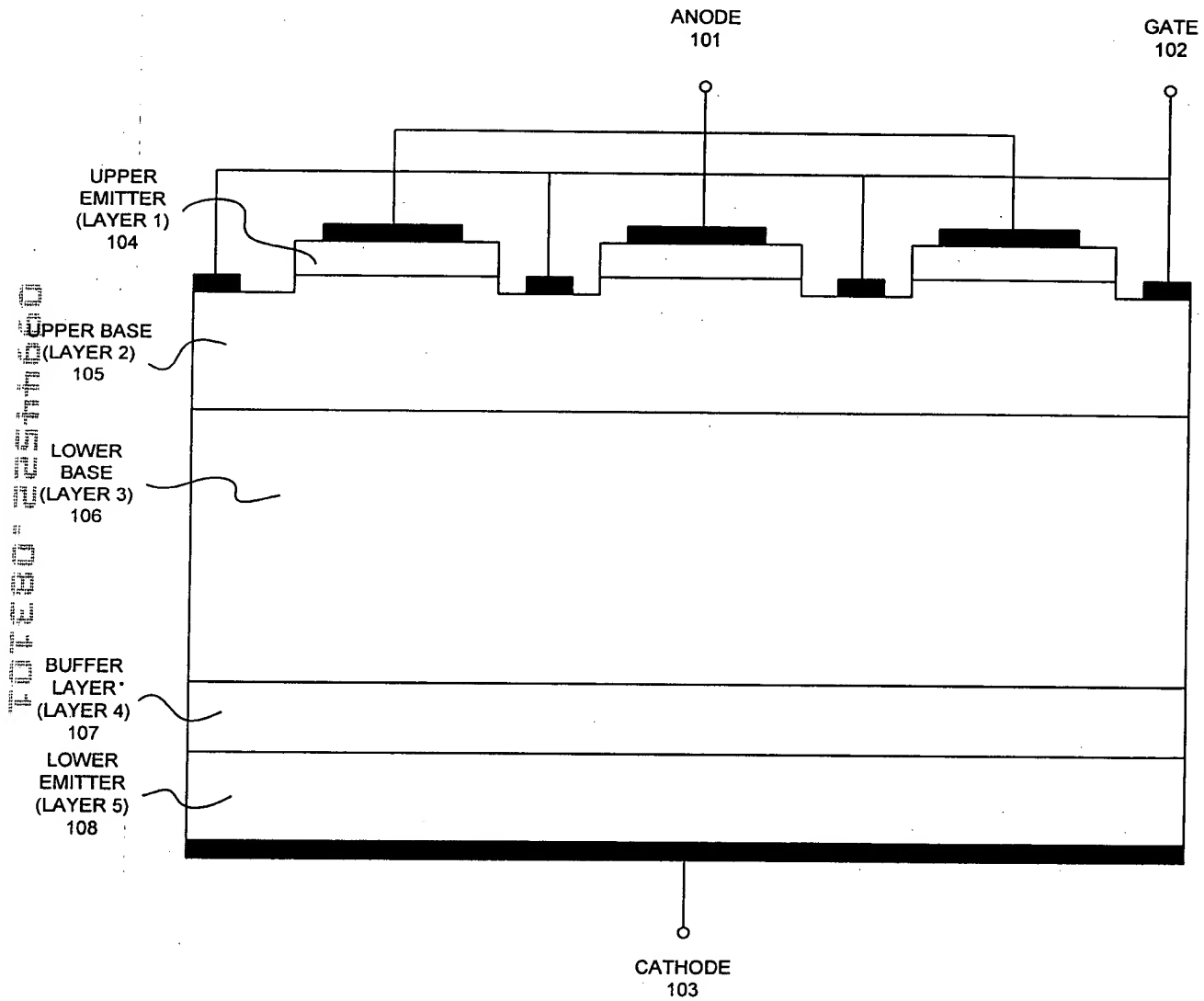


FIGURE 1

DIODE HAVING ONE OR MORE  
DOPED LAYERS

By: Tatsing Paul Chow and Jeffrey Bernard Fedison  
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August 31, 2001  
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DEVICE  
200

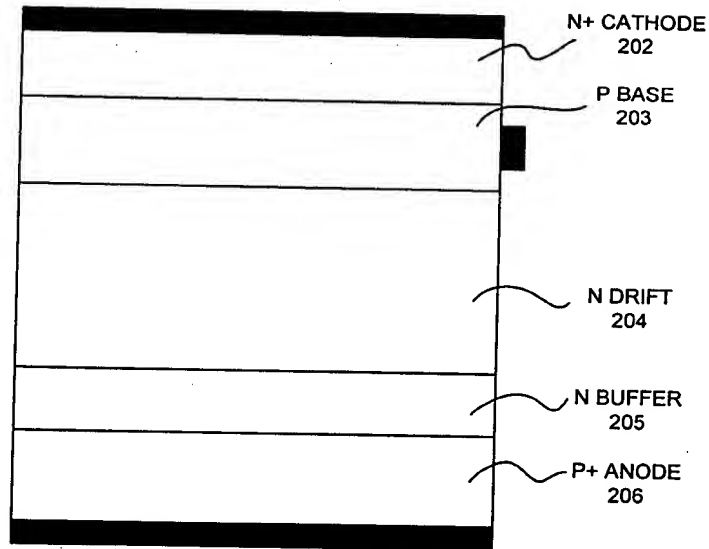


FIGURE 2A

DEVICE  
210

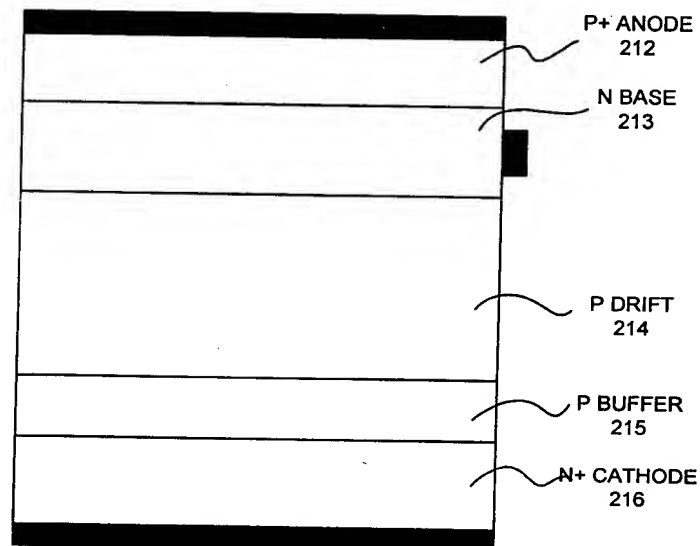


FIGURE 2B

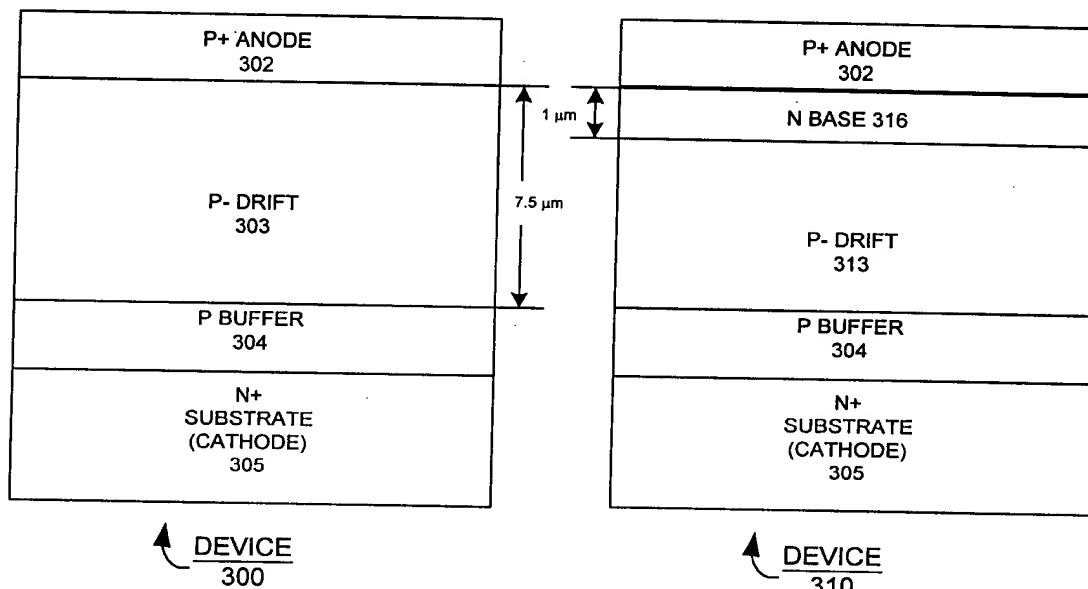
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DIODE HAVING ONE OR MORE  
DOPED LAYERS

By: Tatsing Paul Chow and Jeffrey Bernard Fedison

Attorney Docket No.: R0434/7008 EJL

August 21, 2001

EL 71122202 US

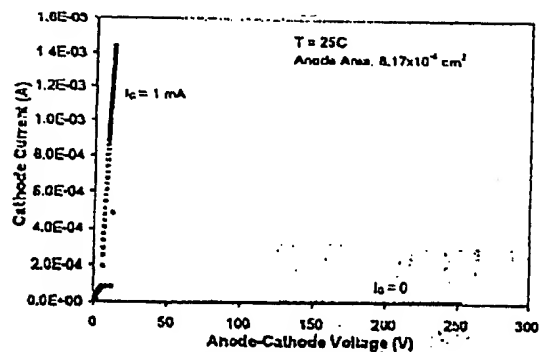


FIGURE 4

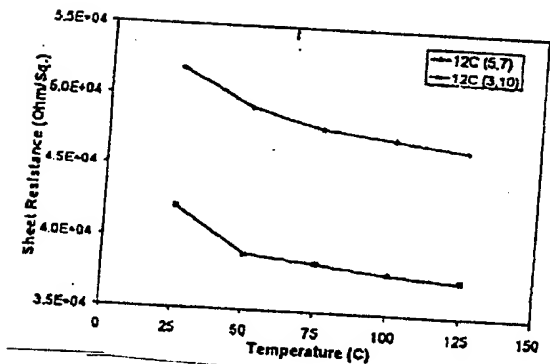


FIGURE 5